



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Docket No.: FIS920030244US1

Dureseti Chidambarao, et al.

Serial No.: 10/687,608

Group Art Unit: 2812

Filed: October 20, 2003

Examiner: Unassigned

For: **STRAINED DISLOCATION-FREE CHANNELS FOR CMOS AND METHOD OF MANUFACTURE**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Sir:

Submitted herewith, are seven (7) sheets of formal drawings comprising figures 1-4 for the above-identified patent application. Please substitute the formal drawings for the drawings which were filed with the application.

Respectfully submitted,

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